**FORM FOR EMPLOYERS**

**REFERENCE NUMBER: 3-IOE-ZTPiT-AD-19**

**INSTITUTION**: Military University of Technology, Institute of Optoelectronics  
**CITY**: Warsaw  
**POSITION**: Assistant lecturer, full-time employment  
**DISCIPLINE**: Electronics  
**POSTED**23.12.2019  
**EXPIRES** **23.01.2020**  
**WEBSITE** [www.wat.edu.pl](http://www.wat.edu.pl)**KEY WORDS**: detection of optical signals, optical signal processing, military optoelectronic devices and systems, infrared cameras.

**DESCRIPTION** (field, expectations, comments):

The candidate should have the knowledge and practical experience in the construction and design of optoelectronic systems for military applications. In addition, the candidate should also have teaching experience (classes in a technical university), as the candidate’s duties will include teaching in classes of “Optoelectronic and imagery reconnaissance” and “Optoelectronics in security systems”.

**I. Admission requirements:**

* meets the requirements determined in art. 113 of the "Law on Higher Education and Science", July 20, 2018, (Journal of Laws of 2018, item 1668);
* at least a M.Sc. degree in the field of electronic or related technical discipline;
* professional experience in the work at technical university;
* professional interests consistent with the teaching and research profile of Infrared Technology and Thermovision Section of the Institute of Optoelectronics, MUT;
* the ability to teach classes in Polish language;
* the ability and experience to operate measurement equipment in the field of electronics and infrared technology;
* the ability to conduct infrared measurements using thermal cameras, confirmed by a training certificate;
* the knowledge required to teach basic courses in the field of electronics and infrared technology (preferred courses : Informatics, Design of digital circuits, Image processing, Computer programming, Infrared measurements, Electronic measurements);
* publications - at least five publications in journals from the B list of the Ministry of Science and Higher Education;
* the ability to use software such as: Matlab, DSP Builder, Altium Designer, Autodesk Inventor, Quartus, Vivado, Active HDL, Keil, Atmel Studio, STM32CubeIDE, QT, Tina, Microsoft Office (Word, Excel ...);
* computer programming skills including Matlab environment and C++ and C programming languages;
* the ability to design complex digital circuits based on FPGA programmable devices using hardware description in Verilog and VHDL;
* the ability to design a schematic and printed circuit boards for digital, analog and mixed systems. Ability to design low-noise analog circuits. Experience in designing fast communication and image transmission interfaces. Ability to design image processing systems;
* the skills to build and run electronic circuits;
* knowledge of image processing the from infrared detectors;
* willingness to participate in scientific and research works;
* confirmed participation in research and development projects;
* documented teaching experience: minimum 400 hours spent on teaching courses in the field of electronics;

**II.** **The competition application should include:**

* a letter of application to the Rector of the Military University of Technology,
* candidate’s questionnaire,
* CV,
* copies of diplomas and other documents confirming / proving the candidate's   
  qualifications,
* a declaration of consent to process personal data included in the job application in accordance with the Data Protection Act,
* a statement that the candidate/applicant has full capacity to act according to the law   
  in force,
* a declaration of no charges within a final judgment of a deliberate crime,
* a declaration of no charges within the disciplinary procedure of the deprivation of rights to practice the profession of academic teacher on a permanent or fixed-term  
  basis/contract,
* a declaration on the full use of civil rights,
* a declaration stating whether MUT is the core or non-core workplace for the applicant.

Document templates available on the website:

<https://bip.wat.edu.pl/index.php/praca/wzory-dokumentow-dla-kandydatow>

**III. Documents should be submitted till 23.01.2020**

- **in person:** to the secretary's office of the Institute of Optoelectronics at the front   
desk, Military University of Technology, Warsaw, 2 gen. Sylwestra Kaliskiego St,   
building 136, room 114;

- **by post**: at the Military University of Technology, Institute of Optoelectronics, 00-908 Warsaw, 2 gen. Sylwestra Kaliskiego St.;

Formal defects that constitute an absolute ground for the rejection of a tender are lack of documents mentioned in point II.

**The application should have a reference number: 3-IOE-ZTPiT-ASD-19**

**IV. Additional information can be obtained by telephone: (+48) 261 83 96 96**

The competition will be adjudicated within two weeks from the deadline for submission of offers.

The competition is the first stage specified in the Statute of the Military University of Technology of employment procedure as an academic teacher, and its positive resolution is the basis for further progress. The final decision on employing a person selected in the course of the competition is made by the Rector. The Military University of Technology reserves the right not to resolve the competition without giving a reason. The University does not provide accommodation.

At the end of the recruitment process, offers that do not meet the formal requirements and all other offers except the offer of the selected candidate are destroyed after one month from the day of the competition procedure completion.